## Features

- Epitaxial Planar Die Construction
- Complementary PNP Type Available (DCP53)
- Ideally Suited for Automated Assembly Processes
- Ideal for Medium Power Switching or Amplification Applications
- Lead Free By Design/RoHS Compliant (Note 1)
- "Green" Device (Note 2)


SOT-223


Schematic and Pin Configuration

- Ordering Information: See Page 3


## Mechanical Data

- Case: SOT-223
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020C
- Terminals: Finish - Matte Tin annealed over Copper leadframe (Lead Free Plating). Solderable per MIL-STD-202, Method 208
- Marking \& Type Code Information: See Page 3
- Weight: 0.115 grams (approximate)


## Maximum Ratings $@ T_{A}=25^{\circ} \mathrm{C}$ unless otherwise specified

| Characteristic | Symbol | Value | Units |
| :--- | :---: | :---: | :---: |
| Collector-Base Voltage | $\mathrm{V}_{\text {CBO }}$ | 100 | V |
| Collector-Emitter Voltage | $\mathrm{V}_{\text {CEO }}$ | 80 | V |
| Emitter-Base Voltage | $\mathrm{V}_{\text {EBO }}$ | 5 | V |
| Collector Current | $\mathrm{IC}_{\mathrm{C}}$ | 1 | A |

## Thermal Characteristics

| Characteristic | Symbol | Value | Unit |
| :--- | :---: | :---: | :---: |
| Power Dissipation @ $\mathrm{T}_{\mathrm{A}}=25^{\circ} \mathrm{C}$ (Note 3) | $\mathrm{P}_{\mathrm{d}}$ | 1 | W |
| Operating and Storage Temperature Range | $\mathrm{T}_{\mathrm{j}}, \mathrm{T}_{\mathrm{STG}}$ | -55 to 150 | ${ }^{\circ} \mathrm{C}$ |
| Thermal Resistance, Junction to Ambient Air $@ \mathrm{~T}_{\mathrm{A}}=25^{\circ} \mathrm{C}$ (Note 3) | $\mathrm{R}_{\theta J \mathrm{~A}}$ | 125 | ${ }^{\circ} \mathrm{C} / \mathrm{W}$ |

Electrical Characteristics $@ T_{A}=25^{\circ} \mathrm{C}$ unless otherwise specified

| Characteristic | Symbol | Min | Typ | Max | Unit | Test Condition |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| OFF CHARACTERISTICS (Note 4) |  |  |  |  |  |  |
| Collector-Base Breakdown Voltage | $V_{\text {(BR) }}$ CBO | 100 | - | - | V | $\mathrm{I}_{\mathrm{C}}=100 \mu \mathrm{~A}, \mathrm{I}_{\mathrm{E}}=0$ |
| Collector-Emitter Breakdown Voltage | $V_{\text {(BR)CEO }}$ | 80 | - | - | V | $\mathrm{I}_{\mathrm{C}}=10 \mathrm{~mA}, \mathrm{I}_{\mathrm{B}}=0$ |
| Emitter-Base Breakdown Voltage | $\mathrm{V}_{\text {(BR)EBO }}$ | 5.0 | - | - | V | $\mathrm{I}_{\mathrm{E}}=10 \mu \mathrm{~A}, \mathrm{I}_{\mathrm{C}}=0$ |
| Collector-Base Cutoff Current | Ісво | - | - | $\begin{aligned} & 0.1 \\ & 20 \end{aligned}$ | $\mu \mathrm{A}$ | $\begin{aligned} & V_{C B}=30 \mathrm{~V}, I_{E}=0 \\ & V_{C B}=30 \mathrm{~V}, I_{E}=0, T_{A}=150^{\circ} \mathrm{C} \end{aligned}$ |
| Emitter-Base Cutoff Current | $\mathrm{I}_{\text {ebo }}$ | - | - | 10 | $\mu \mathrm{A}$ | $\mathrm{V}_{\mathrm{EB}}=5.0 \mathrm{~V}, \mathrm{I}_{\mathrm{C}}=0$ |
| ON CHARACTERISTICS (Note 4) |  |  |  |  |  |  |
| DC Current Gain $\quad$ DCP56-16 | $\mathrm{h}_{\text {FE }}$ | 25 40 25 | 二 | $\overline{250}$ | - | $\begin{array}{ll} \hline \mathrm{IC}=5.0 \mathrm{~mA}, & \mathrm{~V}_{\mathrm{CE}}=2.0 \mathrm{~V} \\ \mathrm{IC}=150 \mathrm{~mA}, & \mathrm{~V}_{\mathrm{CE}}=2.0 \mathrm{~V} \\ \mathrm{I}_{\mathrm{C}}=500 \mathrm{~mA}, & \mathrm{~V}_{\mathrm{CE}}=2.0 \mathrm{~V} \end{array}$ |
|  |  | 100 | 160 | 250 |  | $\mathrm{I}_{\mathrm{C}}=150 \mathrm{~mA}, \mathrm{~V}_{\text {CE }}=2.0 \mathrm{~V}$ |
| Collector-Emitter Saturation Voltage | $\mathrm{V}_{\text {CE(SAT }}$ | - | - | 0.5 | V | $\mathrm{IC}=500 \mathrm{~mA}, \mathrm{I}_{\mathrm{B}}=50 \mathrm{~mA}$ |
| Base-Emitter Turn-On Voltage | $\mathrm{V}_{\text {BE ( }}$ (ON) | - | - | 1.0 | V | $\mathrm{I}_{\mathrm{C}}=500 \mathrm{~mA}, \mathrm{~V}_{\mathrm{CE}}=2.0 \mathrm{~V}$ |
| SMALL SIGNAL CHARACTERISTICS |  |  |  |  |  |  |
| Current-Gain-Bandwidth Product | $\mathrm{f}_{\mathrm{T}}$ | - | 200 | - | MHz | $\begin{aligned} & \text { lc }=50 \mathrm{~mA}, V_{C E}=5.0 \mathrm{~V}, \\ & \mathrm{f}=100 \mathrm{MHz} \end{aligned}$ |

Notes: 1. No purposefully added lead.
2. Diodes Inc.'s "Green" Policy can be found on our website at http://www.diodes.com/products/lead_free/index.php.
3. Device mounted on FR-4 PCB; pad layout as shown on page 4 or in Diodes Inc. suggested pad layout document AP02001, which can be found on our website at http://www.diodes.com/datasheets/ap02001.pdf.
4. Pulse Test: Pulse width $=\leq 300 \mu \mathrm{~s}$, Duty Cycle $\leq 2 \%$.


Fig. 1 Power Dissipation vs. Ambient Temperature


Fig. 3 Typical DC Current Gain vs. Collector Current


Fig. 5 Typical Base Emitter Turn-On Voltage vs. Collector Current


Fig. 4 Typical Collector Emitter Saturation Voltage vs. Collector Current


Fig. 6 Typical Base Emitter Saturation Voltage vs. Collector Current


Fig. 7 Typical Capacitance Characteristics


Fig. 8 Typical Gain-Bandwidth Product vs. Collector Current

## Ordering Information (Note 5)

| Device | Packaging | Shipping |
| :---: | :---: | :---: |
| DCP56-13 | SOT-223 | 2500/Tape \& Reel |
| DCP56-16-13 | SOT-223 | 2500/Tape \& Reel |

Notes: 5. For packaging details, go to our website at http://www.diodes.com/datasheets/ap02007.pdf.

## Marking Information



XXX $=$ Product Type Marking Code ex. N18 = DCP56
N18-16 = DCP56-16
J':' = Manufacturer's Marking Code
YWW = Date Code Marking
$Y=$ Last Digit of Year ex: 7=2007
WW = Week Code 01-52

## Package Outline Dimensions



| SOT-223 |  |  |  |
| :---: | :---: | :---: | :---: |
| Dim | Min | Max | Typ |
| A | 1.55 | 1.65 | 1.60 |
| A1 | 0.010 | 0.15 | 0.05 |
| b1 | 2.90 | 3.10 | 3.00 |
| b2 | 0.60 | 0.80 | 0.70 |
| C | 0.20 | 0.30 | 0.25 |
| D | 6.45 | 6.55 | 6.50 |
| E | 3.45 | 3.55 | 3.50 |
| E1 | 6.90 | 7.10 | 7.00 |
| e | - | - | 4.60 |
| e1 | - | - | 2.30 |
| L | 0.85 | 1.05 | 0.95 |
| Q | 0.84 | 0.94 | 0.89 |
| All Dimensions in $\mathbf{~ m m}$ |  |  |  |

## Suggested Pad Layout: (Based on IPC-SM-782)



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